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PN - WO9406077 A 19940317

PD - 1994-03-17

PR - DE19924228761 19920828

OPD - 1992-08-28

TI - COMPUTER SYSTEM WITH AT LEAST ONE MICROPROCESSOR AND AT LEAST ONE COPROCESSOR, AND A METHOD OF OPERATING THE SYSTEM

AB - The invention concerns a computer system with at least one microprocessor and a coprocessor, the coprocessor (COP) being hardware configured so that the function performed by the coprocessor is adjustable in dependence on the program to be executed. The coprocessor is made up of functional units which each can execute one function and programmable wiring channels which permit a connection to be established between each functional unit and the microprocessor. When an application program is executed, it is thus possible to configure one or more coprocessors according to the program code and hence accelerate program execution.

IN - BUCHENRIEDER KLAUS (DE)

PA - SIEMENS AG (DE);BUCHENRIEDER KLAUS (DE)

EC - G06F9/38S4 ; G06F15/78R

IC - G06F9/38 ; G06F15/78

CT - EP0497029 A [X]; US4694416 A [A]; US4829380 A [A]

CTNP- [A] IEEE SOUTHEASTCON 87 Bd. 1 , 5. August 1987 , TAMPA, USA
Seiten 225 - 228 M. C. ERTEM 'A reconfigurable co-processor for microprocessor systems';

- [A] IEEE 1990 CUSTOM INTEGRATED CIRCUITS CONFERENCE Mai 1990
Seiten 3141 - 3144 N. HASTIE AND R. CLIFF 'The implementation
of hardware subroutines on field programmable gate arrays';

- [A] IEEE 1987 CUSTOM INTEGRATED CIRCUITS CONFERENCE Juli 1987
Seiten 149 - 152 J. ROWSON ET AL 'A datapath compiler for standard cells and gate arrays';

- [ ] See also references of EPA 0657044

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TI - Computer system based upon microprocessor and configurable coprocessor - has configurable coprocessor produced as

hardwired module with functional units that can be selected based upon result of programme compilation

PR   - DE19924228761 19920828

PN   - WO9406077 A1 19940317 DW199412 G06F9/38 013pp
     - EP0657044 A1 19950614 DW199528 G06F9/38 Ger 013pp
     - EP0657044 B1 19960207 DW199610 G06F9/38 Ger 005pp
     - DE59301609G G 19960321 DW199617 G06F9/38 000pp
     - ES2083296T T3 19960401 DW199621 G06F9/38 000pp

PA   - (SIEI) SIEMENS AG

IC   - G06F9/38 ;G06F15/78

IN   - BUCHENRIEDER K

AB   - WO9406077 A computer system consists of a microprocessor (MP) that performs the central processing function, a coprocessor (COP) and a configuration unit (KE), of which are coupled to a bus. The coprocessor is produced as a hardwired module and has a facility to allow the configuration to adjust when the programme to be executed has been compiled.
     - The configurable coprocessor consists of a number of modules each providing functions that can be selected using a programmable gate array. The selection is based upon the result of the programme compilation and uses the configuration unit.
     - ADVANTAGE - Allows reconfiguration of coprocessor for improved processing speed.
     - (Dwg.1/2)

EPAB - EP-657044 Method of operating a computer system comprising at least one microprocessor (MP) and coprocessors (COP), in which the coprocessor (COP) is made up of a plurality of functional units (F(i)), each of which is specialized for the execution of a function, in which, for a user program to be processed, the command sequences which can be executed by a coprocessor are established during the compiling of said program, and in which according to the established command sequence, one of the functional units of the coprocessor is selected and connected to the microprocessor (MP).(Dwg.1/2)

OPD - 1992-08-28

CT   - 03Jnl.Ref;EP0497029;US4694416;US4829380

DN   - US

DS   - AT BE CH DE DK ES FR GB GR IE IT LI LU MC NL PT SE

AN   - 1994-101399 [21]